

PART B - FEE(S) TRANSMITTAL



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20350 7590 12/03/2002

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Lata Olivier	(Depositor's name)
<i>Lata Olivier</i>	(Signature)
3/3/03	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/742,816	12/19/2000	Jong-Hee Han	0939H081300	7332

TITLE OF INVENTION: DELAY LOCKED LOOP FOR USE IN SEMICONDUCTOR MEMORY DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1280	\$300	\$1580	03/05/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
HO, HOAI V	2818	365-194000

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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1. ~~AND CREW LLP~~

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Hynix Semiconductor

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Seoul, Korea

Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☐ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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(Authorized Signature) *Cathy E. Cretzinger* (Date) 3/3/03
Cathy E. Cretzinger, #51,588

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03/14/2003 RNEBRAH1 00000186 201430 09742816

01 FC:1501	1300.00 CH
02 FC:1504	300.00 CH
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